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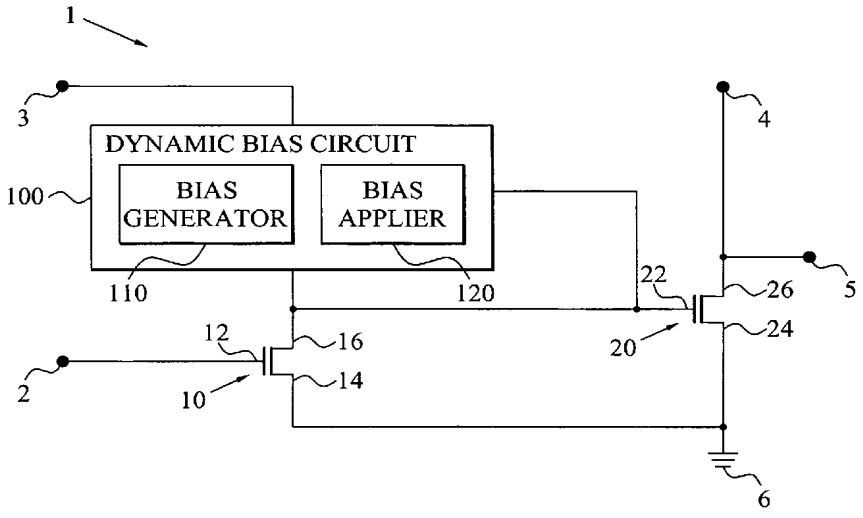
(54) Title: DYNAMICALLY BIASED AMPLIFIER



(57) Abstract: The present invention provides dynamic biasing of transistor in an amplifier (1) comprising at least two interconnected transistor (10, 20) provided for processing an input signal. Once the input signal is applied to a driver transistor (10), a DC current signal of the output electrode (16) of this transistor (10) is detected. This DC current detection could be implemented as a detection of a voltage drop by providing the DC current signal to a resistor (130). A dynamic bias signal is then generated based on this detected DC current signal or voltage drop proportional to the DC current signal. The bias signal is applied to an input electrode (22) of a final transistor (20) for providing dynamic biasing thereof. The biasing of the invention reduces the intermodulation distortion of the final transistor (20) and amplifier (1). In addition, the biasing enables an automatic change of operation class for the transistor (20).

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